



Mr. Arunkumar G V

(IBM Research, USA)

Arunkumar is a CMP Process Engineer at IBM Research, Albany. His research focuses on developing Chemical Mechanical Planarization (CMP Process) for advanced semiconductor nodes, focusing mainly on process optimization, testing, and characterizing new CMP consumables, and analyzing inline and electrical performance of the device, and bringing new eco-friendly materials to develop CMP Process. Prior to this, he worked at GlobalFoundries as a CMP Process Engineer in a HVM fab, on 14nm FinFET technology. He received his MS degree from Clarkson University in Electrical Engineering (2022), and Bachelor of Engineering from Visvesvaraya Technological University (2020). During his master's degree at Clarkson University, he worked on developing CMP Slurries and Post-CMP cleaning solutions for semiconductor applications.