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Dr. Baek is a Principal Engineer in the Foundry Process Development team at Samsung Electronics. He received the Ph.D. degree in the department of Physics from KAIST in 2004. Since joining Samsung Electronics Semiconductor R&D Center in 2010, He has been involved in working on the next-generation photolithography for Logic Devices.

He is currently leading the R&D of Photolithography development for Logic Devices. His current research interests mainly focus not only on EUV lithography for Logic Device, but also on various types of lithography for Next generation Devices.