



## **Dr. Jaegil Lee**

(Powermaster Semiconductor Co., LTD., Korea)

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Jaegil Lee received his B.S. degree in Electrical Engineering from Yonsei University in 1994, and his M.S. and Ph.D. degrees in Electrical Engineering from Pohang University of Science and Technology (POSTECH) in 1997 and 2001, respectively. From 2001 to 2016, he was engineer for high voltage MOSFET development in Fairchild Semiconductor. From 2016 to 2018, he focused on his research field to super-junction MOSFET in ON Semiconductor. Since 2018, he has managed research & tech. development team in Powermaster Semiconductor. Now he is the CEO of Powermaster Semiconductor. Regarding his research field, he holds more than 50 patents including 22 U.S. patents.