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Jeonghoon Lee is a Principal Member of the Technical Staff in the Computational Patterning Team (CPT) at imec. He received his Master's degree in Metallurgical Engineering from Hanyang University in 1999. In August 1999, he began his career as a lithography engineer at Samsung Electronics, where he was later promoted to Principal Engineer with Patterning Strategy staff and served as a Module leader of Logic OPC/Project Leader of VNAND OPC part in the Process Development Team of the Device Solutions (DS) Division until February 2020. In March 2020, he joined imec, where he is currently working on several patterning projects as a Principal Member of the Technical Staff.